

FIG. 1  
PRIOR ART

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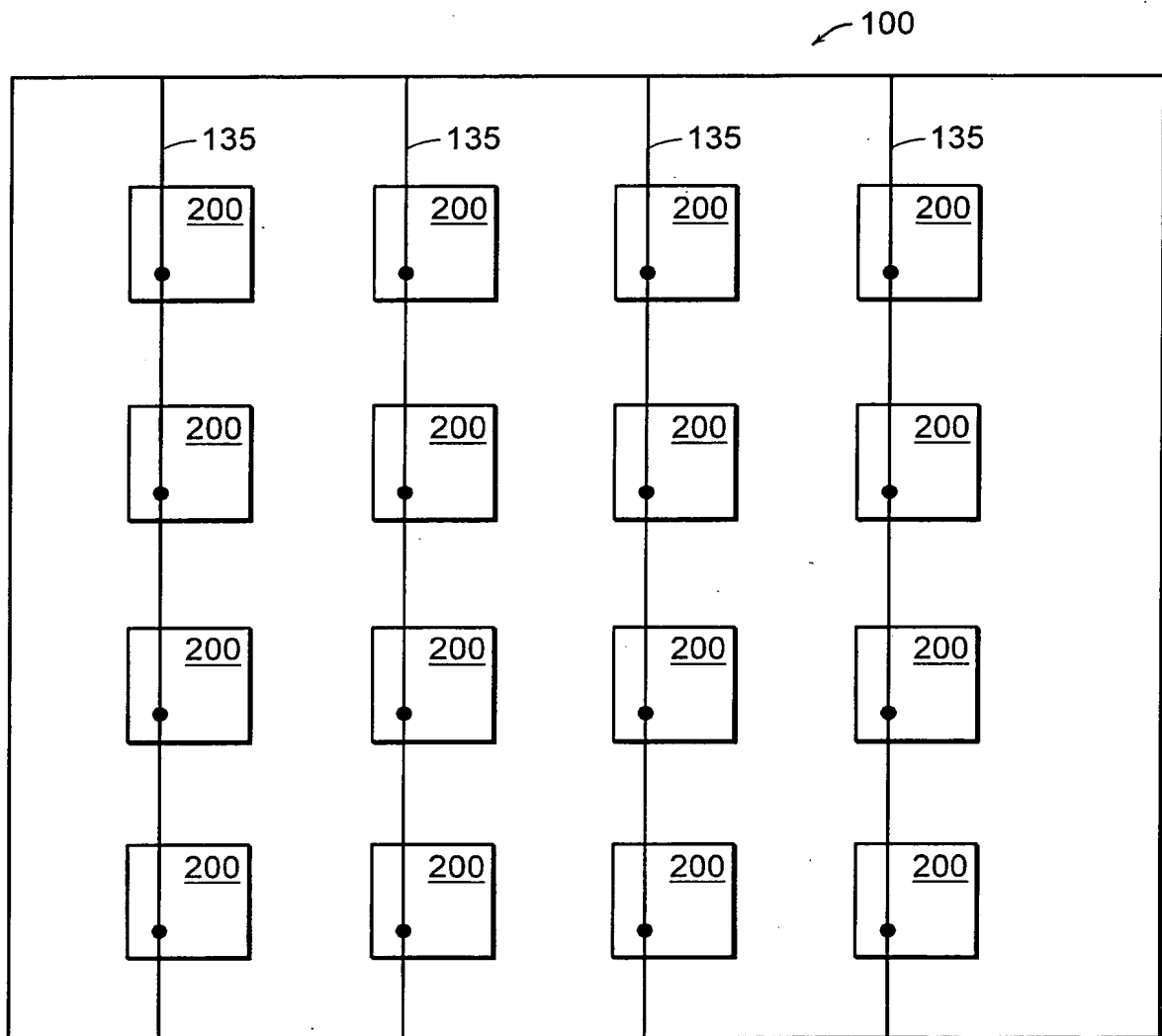


FIG. 2A  
PRIOR ART

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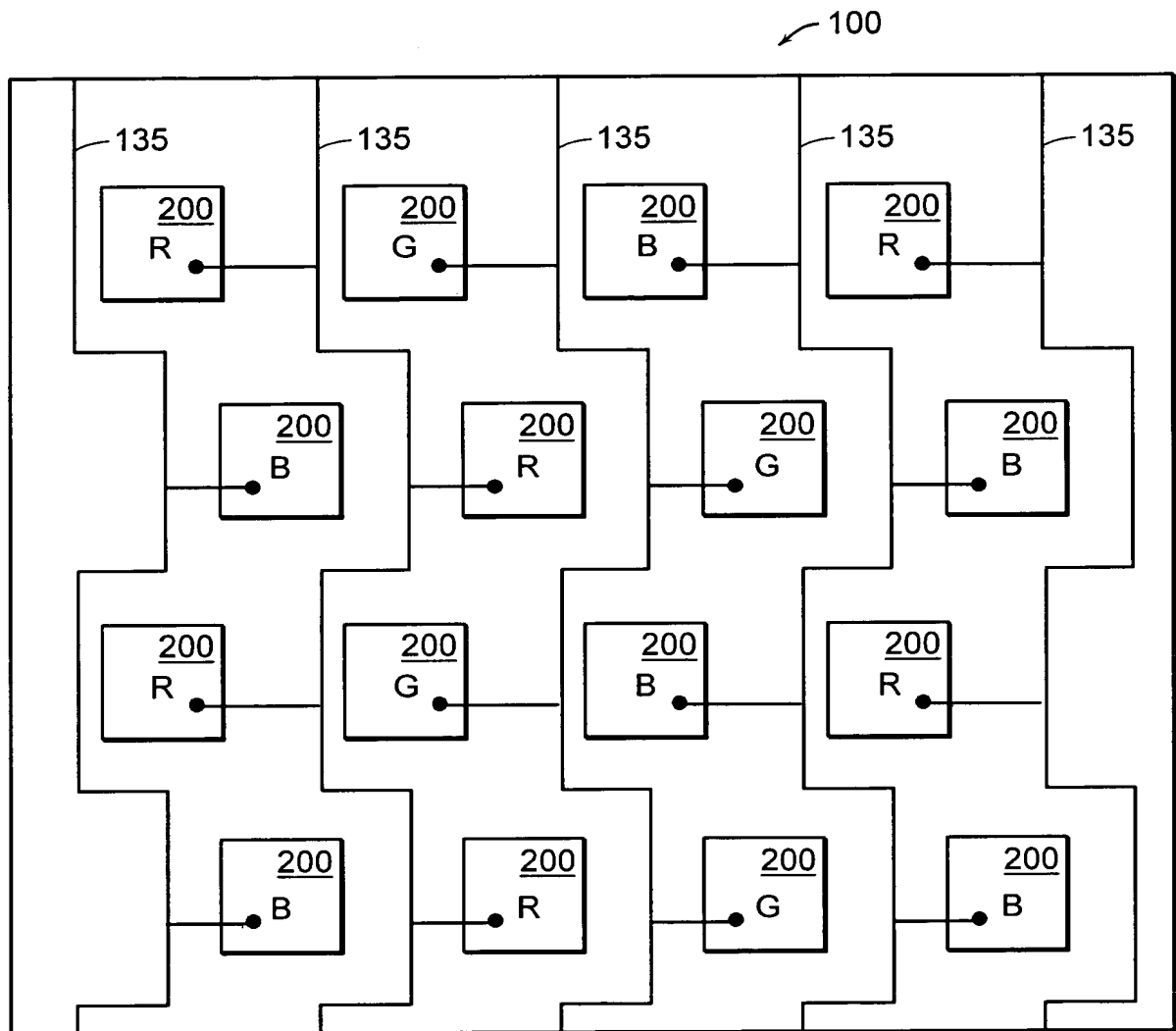


FIG. 2B  
PRIOR ART

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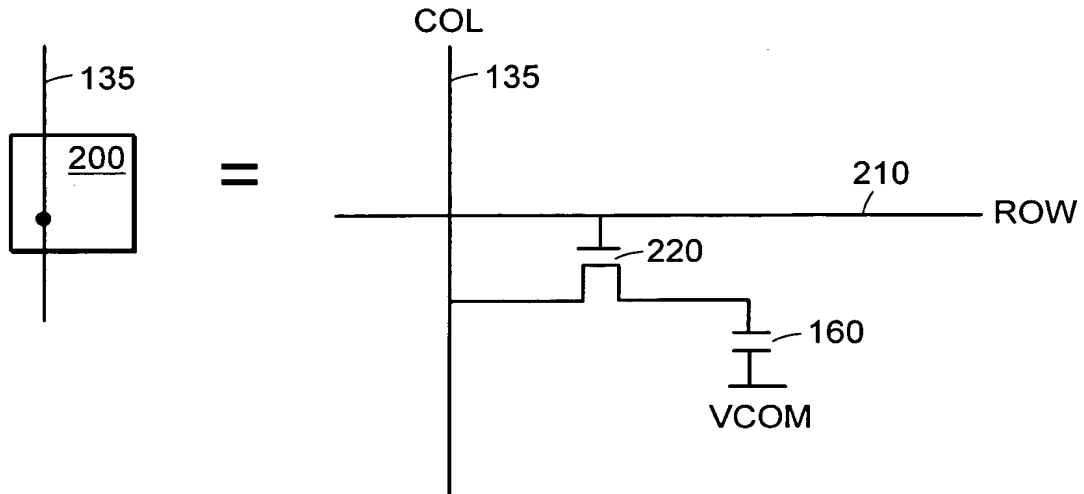


FIG. 2C  
 PRIOR ART

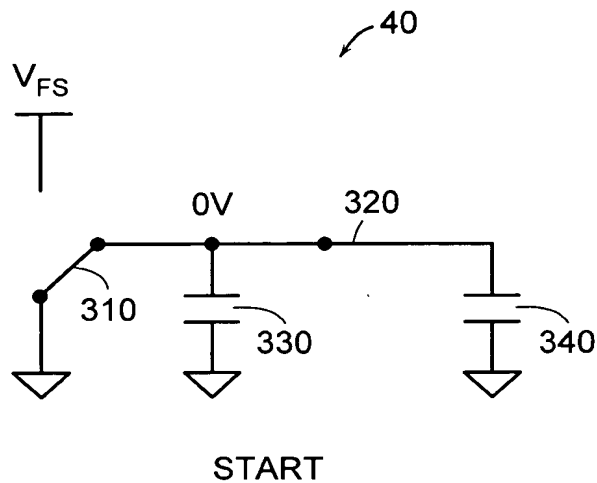
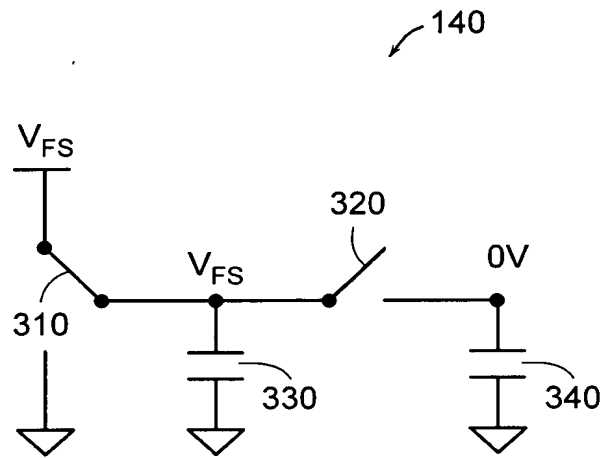


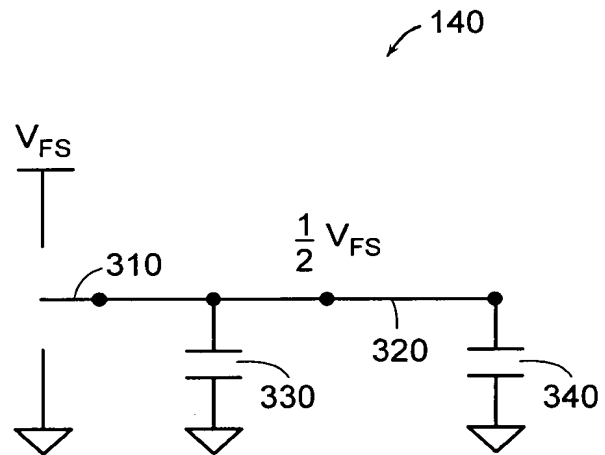
FIG. 3A  
 PRIOR ART

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LSB = 1

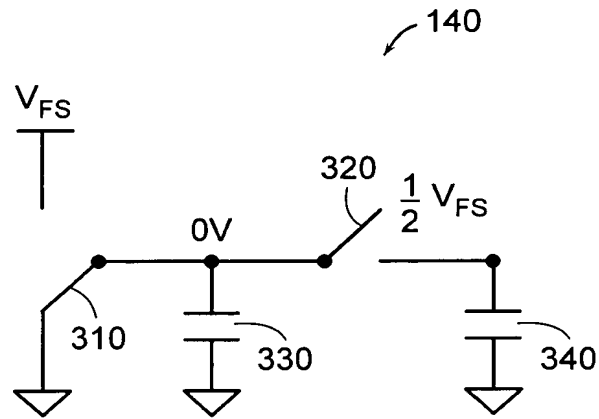
**FIG. 3B**  
PRIOR ART



EQUALIZE

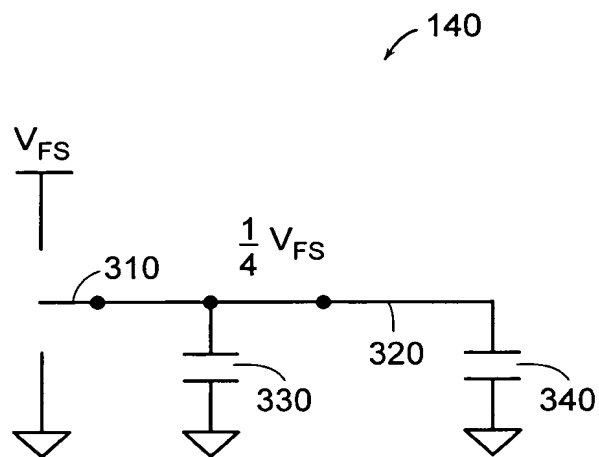
**FIG. 3C**  
PRIOR ART

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Bit<sub>1</sub> = 0

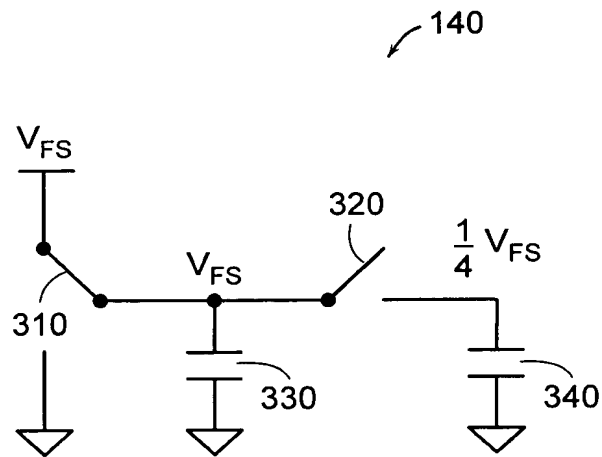
FIG. 3D  
PRIOR ART



EQUALIZE

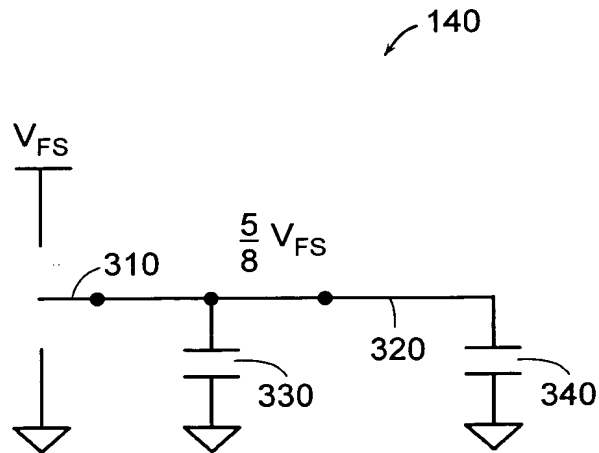
FIG. 3E  
PRIOR ART

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Bit<sub>2</sub> = 1

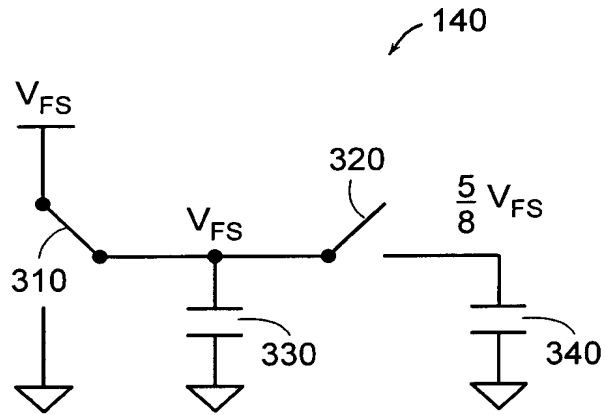
**FIG. 3F**  
PRIOR ART



EQUALIZE

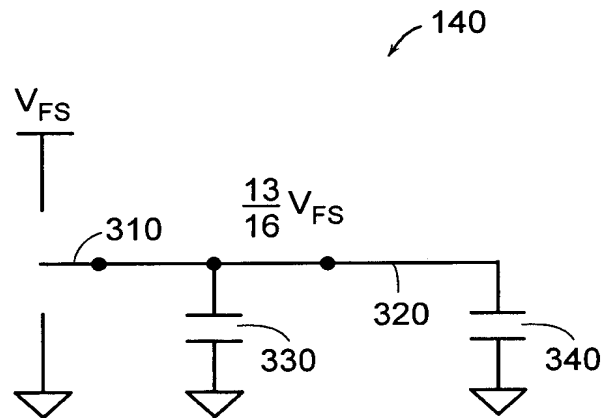
**FIG. 3G**  
PRIOR ART

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MSB = 1

FIG. 3H  
PRIOR ART



DONE

FIG. 3I  
PRIOR ART



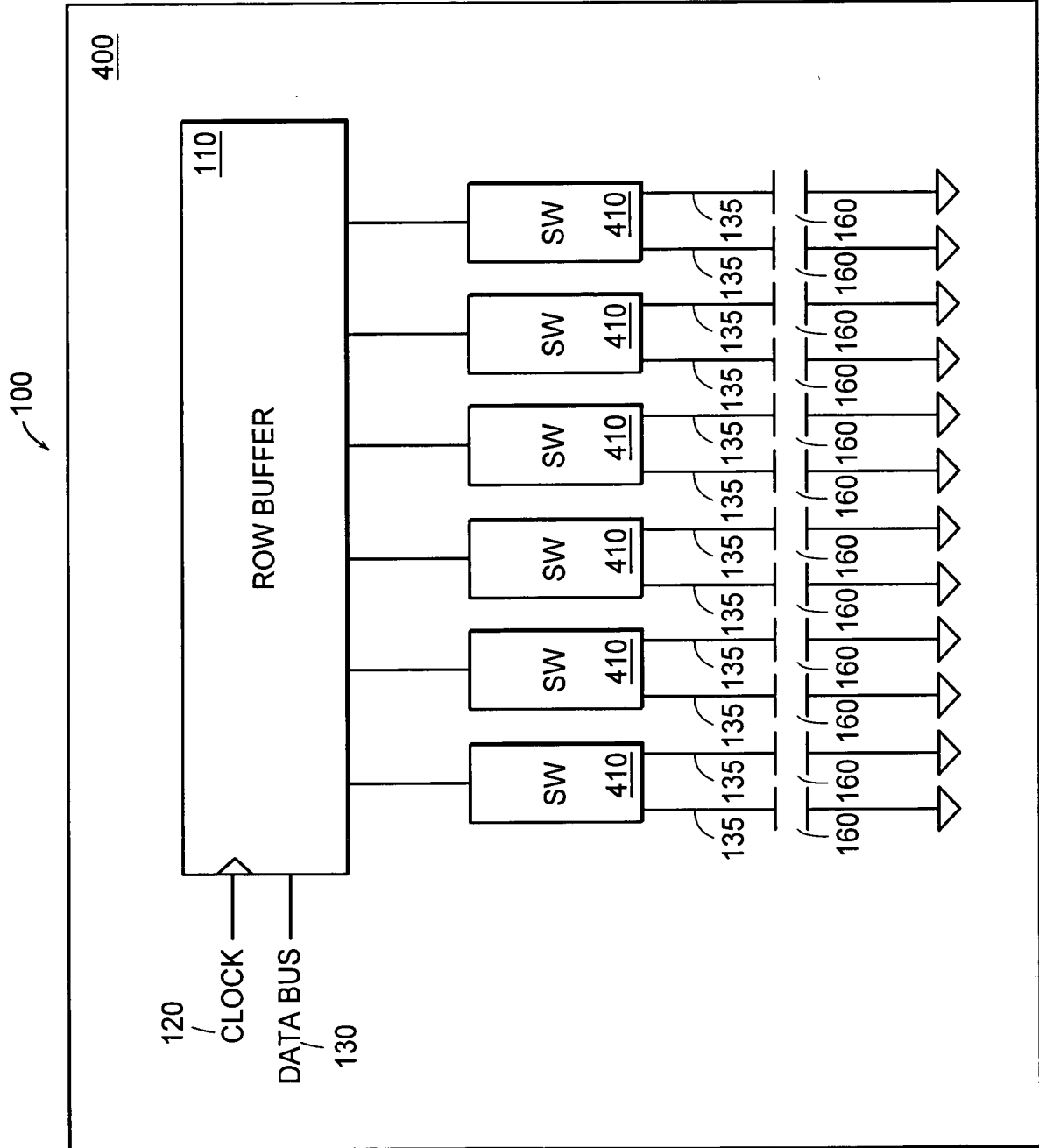


FIG. 4

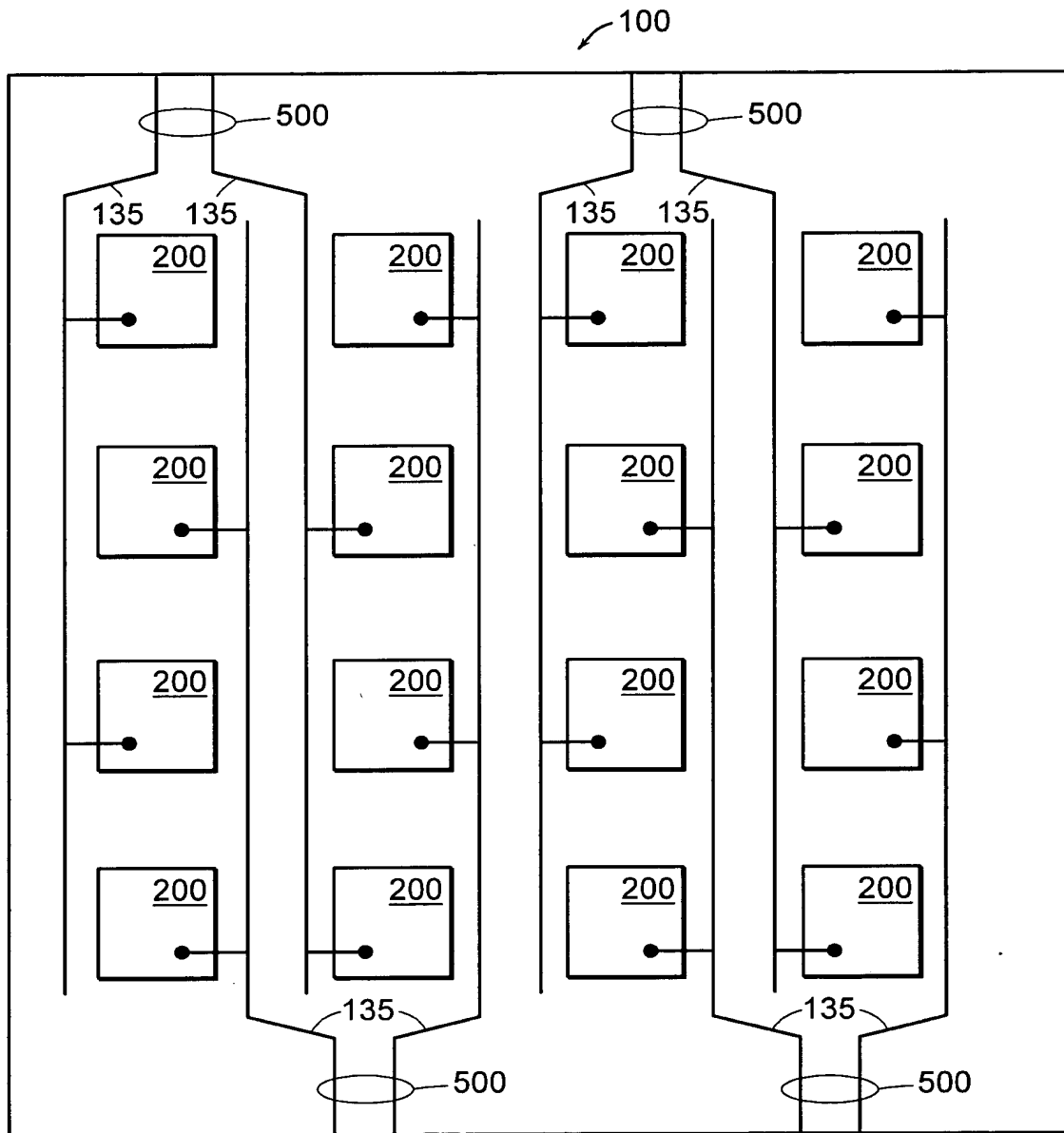


FIG. 5A

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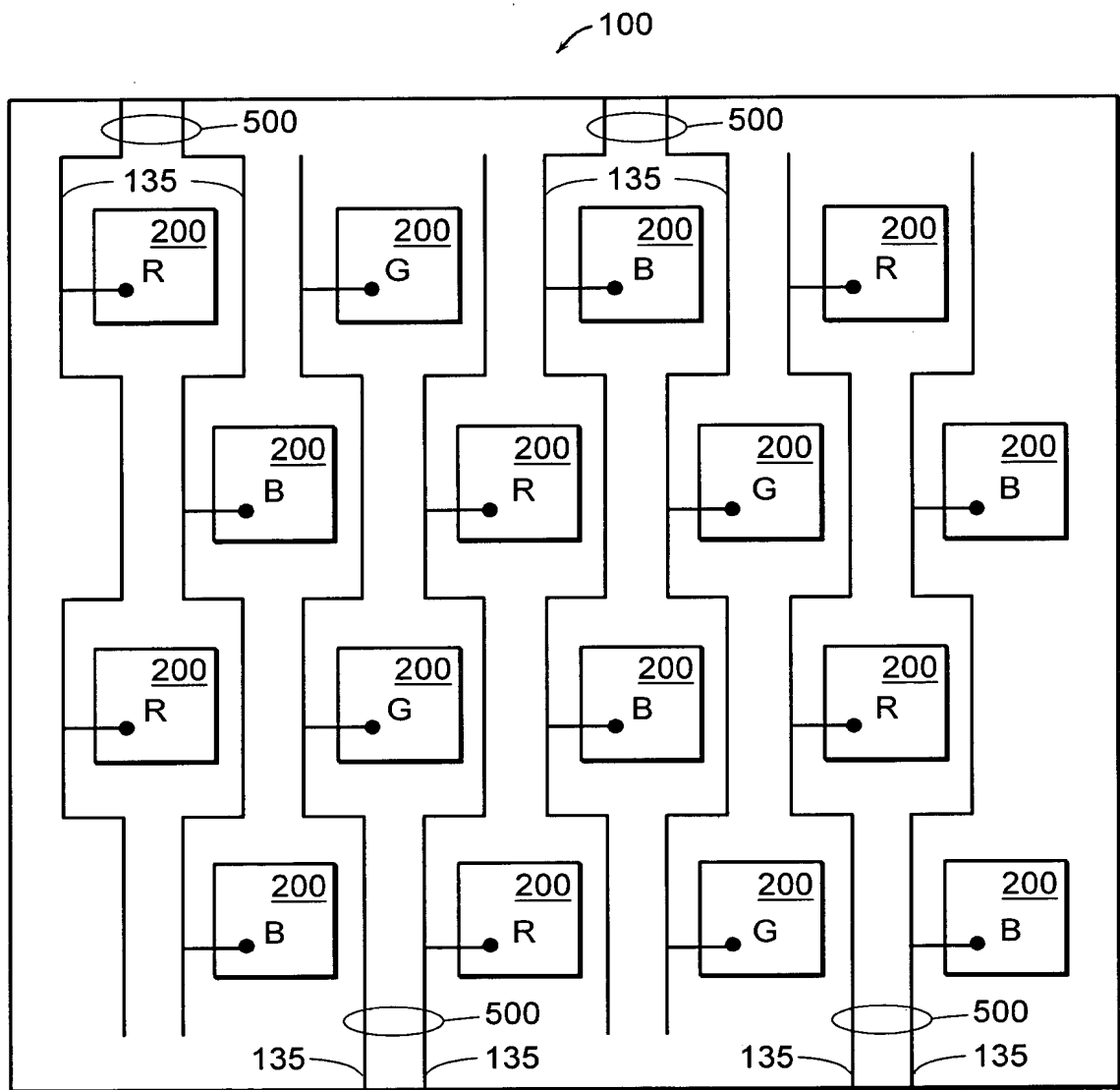


FIG. 5B

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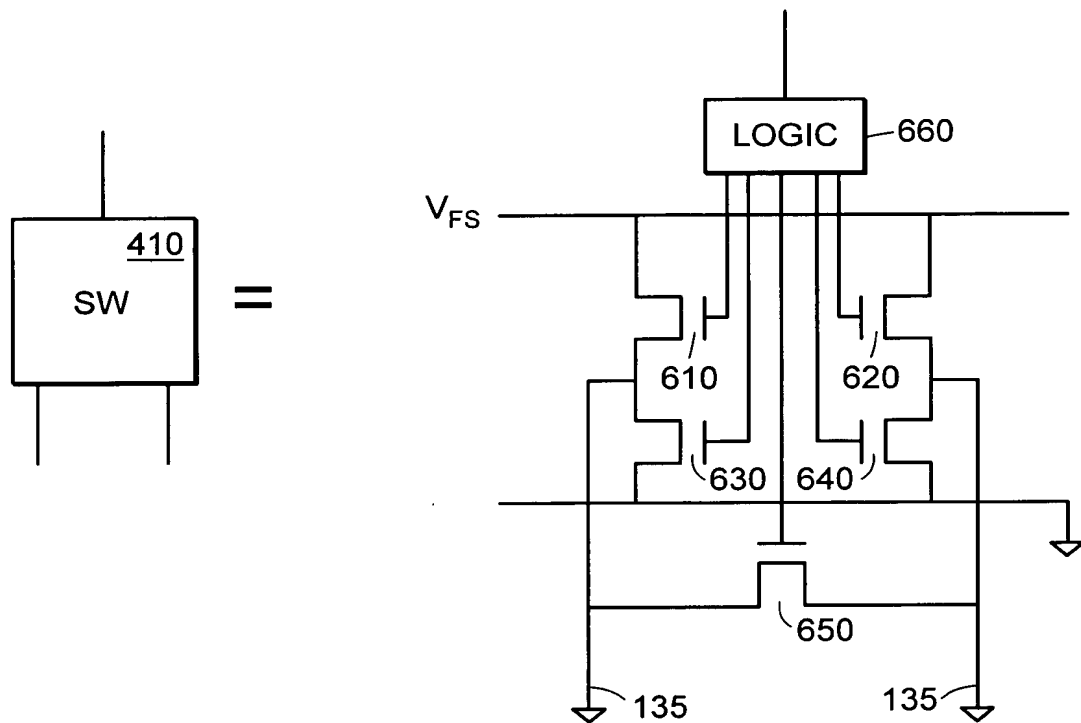


FIG. 6